

10/529673

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Attorney Docket # 5367-167PUS

JC17 Rec'd PCT/PTO 29^{Patent} MAR 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re National Phase PCT Application of

Dominik EISERT et al.

Int'l PCT Serial No.: PCT/DE2003/002954

Int'l Filing Date:: 5 September 2003

For: Semiconductor Component and Production
Method

INFORMATION DISCLOSURE STATEMENT

Mail Stop **PCT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

S I R:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed Form PTO SB/08A.

In accordance with 37 C.F.R §§1.97(g) and (h), the filing of this Information Disclosure Statement should not be construed as a representation that a search has been made or that information cited is, or is considered to be, material to patentability as defined in §1.56(b), or that any cited document listed or attached is (or constitutes) prior art. Unless otherwise indicated, the date of publication indicated for an item is taken from the face of the item and Applicants reserve the right to prove that the date of publication is in fact different.

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Submitted herewith is a copy of the International Search Report issued in connection with International Application on which the present U.S. National Phase application is based.

Copies of the references cited in the International Search Report have been supplied by WIPO to the USPTO. Therefore, copies of the cited references in the International Search Report are not being provided herewith in accordance with current U.S. practice because they should be on file already at the USPTO.

It is respectfully requested that the Examiner return an initialed copy of the attached Form PTO SB/08A to confirm that the publications supplied by WIPO and listed therein have been considered and made of record.

Respectfully submitted,

COHEN, PONTANI, LIEBERMAN & PAVANE

By Thomas Langer
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Dated: March 29, 2005

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	10/529673
		Filing Date	
		First Named Inventor	Dominik EISERT
		Art Unit	
		Examiner Name	
Sheet 1 of 2	Attorney Docket Number		5367-167PUS

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind-Code ² (if known)			
		US-2001/004534 A1	06/21/2001	Carter-Coman et al.	
		US-2001/050376 A1	12/13/2001	Asami et al.	
		US-2002/137244 A1	09/26/2002	Chen et al.	
		US-6,335,546 B1	01/01/2002	Tsuda et al.	
		US-4,862,239 A	08/29/1989	Broich et al.	
		US-2003/183835 A1	10/02/2003	Yanagihara et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind-Code ⁵ (if known)				
		EP 0 184 117 A	06/11/1986	Siemens AG		
		DE 199 05 517 A	12/09/1999	Hewlett Packard Co.		
		WO 01/61766 A	08/23/2001	Yanagihara et al.		
		DE 100 40 448 A	03/07/2002	Osram Opto Semiconductors GmbH		
		WO 01/41225 A	06/07/2001	Cree Lighting Co.		

Examiner Signature		Date Considered	
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Substitute for Form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/529673
		Filing Date	
		First Named Inventor	Dominik EISERT
		Art Unit	
		Examiner Name	
Sheet 2 of 2	Attorney Docket Number	5367-167PUS	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Naoki Wada et al., "Stable Operation of AlGaAs/GaAs Light-Emitting Diodes Fabricated on Si Substrate" Japanese Journal of Applied Physics, Vol. 31, Nr. 2A Part 2, pp. L78-L81, February 1, 1992	
		Wada N. et al., "GaAs/AlGaAs Light Emitters Fabricated on Undercut GaAs on Si", Japanese Journal of Applied Physics, Vol. 33, Nr. 3A, pp. 1268-1274, March 1, 1994	
		Olsen et al., "Calculated stresses in multilayered heteroepitaxial structures", Journal of Applied Physics, Vol. 48, Nr. 6, pp. 2543-2547	
Examiner Signature		Date Considered	

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